


IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application) PATENT APPLICATION
Inventors: OM P. AGRAWAL et al.)
Serial No.: 09/235,615) Art Unit: 2819
Filed: January 21, 1999)
Title: FPGA INTEGRATED CIRCUIT HAVING EMBEDDED)
SRAM MEMORY BLOCKS WITH REGISTERED)
ADDRESS AND DATA INPUT SECTIONS)

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Gideon Gimlan, Esq., Reg. No. 31,855
Signature Date: February 24, 1999

(Attorney Signature)

REQUEST FOR CORRECTED FILING DATE AND FILING RECEIPT

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Washington, D.C. 20231

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It is hereby respectfully requested that the Patent Office correct its records to indicate a filing date of January 21, 1999 and issue a corrected Filing Receipt.

- 1 -

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Respectfully submitted,

Date: February 24, 1999

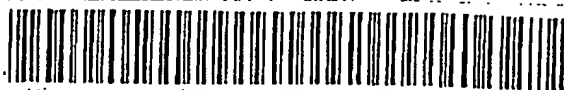
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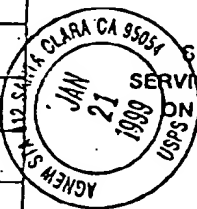
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